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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Suresh Balasubramanian

Docket Number: TI-33469

Serial No.: 10/015,741

Art Unit: 2816

Filed: 12/17/01

Examiner: Cassandra F. Cox

For: Generating a Lock Signal Indicating Whether an Output
Clock Signal Generated by a PLL is in Lock with an Input
Reference Signal

CERTIFICATION OF FACSIMILE TRANSMISSION

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<input type="checkbox"/> CONTINUATION APP'N	
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NAME OF INVENTOR(S): Suresh Balasubramanian	
TITLE OF INVENTION: Generating a Lock Signal Indicating Whether an Output Clock Signal Generated by a PLL is in Lock with an Input Reference Signal	
TI FILE NO.: TI-33469	DEPOSIT ACCT. NO.: 20-0668
FAXED: 9-21-05 DUE: 9-29-05 ATTY/SECY: AKS/kjv	
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For: GENERATING A LOCK SIGNAL INDICATING WHETHER AN OUTPUT CLOCK
SIGNAL GENERATED BY A PLL IS IN LOCK WITH AN INPUT REFERENCE SIGNAL

Commissioner of Patents
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AMENDMENT

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29, 2005, please enter the following:

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